

a memory in which a second program as a write control program for a writing to the ROM is stored; and

an input and output unit;

wherein the central processing unit performs a writing to the ROM of the first program or the data input from outside of the semiconductor chip via the input and output unit by controlling the write control circuit based on the write control program,

wherein the write control program includes an instruction which changes a process of the central processing unit to a process that controls a writing of the ROM based on the write control program stored in the memory, and

wherein the write control program includes an instruction which returns the process of the CPU to a process based on the program stored in the ROM after completion of the process that controls the writing of the ROM.

32. (Once Amended) A microcomputer according the claim 31,

wherein the memory which stores the write control program is a mask ROM.